

XC74WL Series

CMOS Logic

■ GENERAL DESCRIPTION

The XC74WL series is manufactured using silicon gate CMOS processes.

The small supply current, which is one of the features of the CMOS logic, gives way to high speed operations which enables LS-TTL. To be surpassed.

Since it is possible to input a voltage higher than the reference voltage, it is also possible to achieve a level change from 5V to 3V.

With wave forming buffers connected internally, stabilized output can be achieved as the series offers high noise immunity.

As the series is integrated into a mini molded, MSOP-8B package, high density mounting is possible.

■ APPLICATIONS

- Palmtops
- Digital equipment

■ FEATURES

- CMOS Logic
- Operating Voltage Range : 2V ~ 5.5V
- Input Voltage Range : 0V ~ 5.5V
- 5V to 3V Level Change
- High Speed Operations
- Low Power Consumption
- MSOP-8B Package

■ PRODUCT LIST

| SERIES | FUNCTION |
|--------------|--|
| XC74WL00AASR | Dual 2-Input NAND Gate |
| XC74WL02AASR | Dual 2-Input NOR Gate |
| XC74WL04AASR | Triple Inverter |
| XC74WL08AASR | Dual 2-Input AND Gate |
| XC74WL14AASR | Triple Schmitt Inverter |
| XC74WL32AASR | Dual 2-Input OR Gate |
| XC74WL34AASR | Triple Buffer |
| XC74WL74AASR | D-type Flip-Flop |
| XC74WL86AASR | Dual 2-Input Exclusive-OR Gate |
| XC74WL125ASR | Dual Bus Buffer |
| XC74WL126ASR | Dual Bus Buffer |
| XC74WL157ASR | 2-Channel Multiplexer |
| XC74WL240ASR | Dual Bus Buffer (Inverted 3-State Outputs) |
| XC74WL241ASR | Dual Bus Buffer (Non Inverted 3-State Outputs) |
| XC74WL4053SR | 2-Channel Analog Multiplexer/Demultiplexer |
| XC74WL4066SR | Dual Analog Switch |
| XC74WLU04ASR | Triple Inverter (Unbufferd) |

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