

## ● LGA-6B01 Power Dissipation (JESD51-7)

Power dissipation data for the LGA-6B01 is shown in this page.

The value of power dissipation varies with the mount board conditions.

Please use this data as one of reference data taken in the described condition.

### 1. Measurement Condition (Reference data)

Condition : Mount on a board

Ambient : Natural convection

Soldering : Lead (Pb) free

Board : The board using 4 copper layer.

(76.2mm × 114.3mm ••• Area: about 8700mm<sup>2</sup>)

1st layer : No copper foil (Signal layer)

2nd layer : 70mm × 70mm\_Connected to heat-sink.

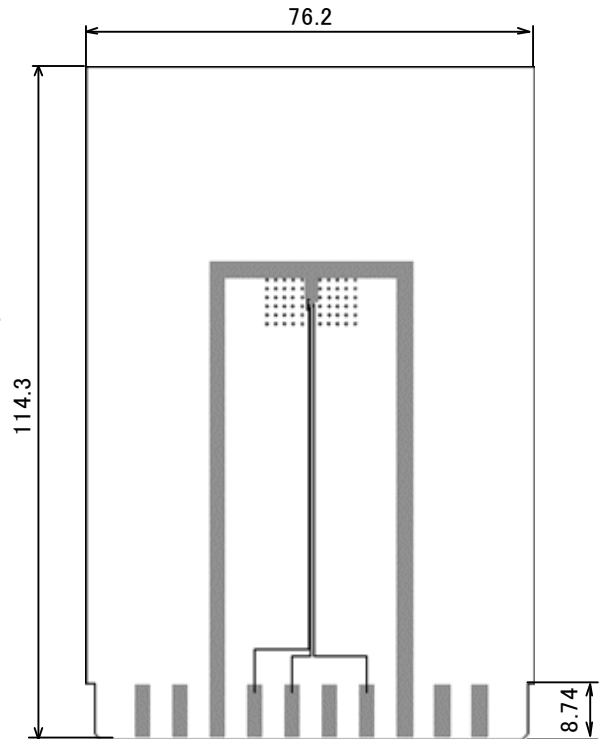
3rd layer : 70mm × 70mm\_Connected to heat-sink.

4th layer : No copper foil (Signal layer)

Material : Glass Epoxy (FR-4)

Thickness : 1.6mm

Through-hole  $\phi$ 0.2mm x 60pcs



### 2. Power Dissipation vs. Ambient temperature

Board Mount ( $T_{jmax} = 125^{\circ}\text{C}$ )

Ambient Temperature ( $^{\circ}\text{C}$ )	Power Dissipation Pd (mW)	$\theta_{ja}$ ( $^{\circ}\text{C}/\text{W}$ )
25	760	131.58
105	152	

